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In 1963, Frank Wanlass and C.T.Sah of Fairchild unveiled the first logic gate in which n-channel and p-channel transistors were used in a complementary symmetric circuit configuration. This is what is ...

A Review Paper on CMOS, SOI and FinFET Technology

Providing a cutting-edge and effective overview of the principles and techniques for designing circuits, this systematic text enables readers to undertake the design of an analog circuit that can be ...

CMOS Analog Circuit Design

The design of both the pitch and ... Lopez C.M. A Compact Quad-Shank CMOS Neural Probe With 5,120 Addressable Recording Sites and 384 Fully Differential Parallel Channels. IEEE Trans Biomed Circuits ...

Recording the Brain at Work with Thousands of Sensors

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And we have the first Raspberry Pi Zero hack! In less than 72 hours from the official release announcement [Shintaro] attached an Edimax WiFi USB Adapter directly to the USB solder pads on the P ...

First Raspberry Pi Zero Hack – Piggy-Back WiFi.

Maybe [Paul Scott] and [Allen Versfeld]'s Tiny Radio Telescope project will change that view. The NRAO published a radio telescope design a few years ago for use mainly as an educational tool ...

The Tiny Radio Telescope

Earlier this month at the 2014 Symposia on VLSI Technology and Circuits (Honolulu, HI; June 9 to 13, 2014), Sony Corp. announced the development of spherically curved CMOS sensors. % {[...

Curved image sensors created by bending

By using the latest addition to the X-FAB reference design kit portfolio, it is now possible to show the set-up of the PDK for the company's XH018 180 nm modular mixed-signal high-voltage CMOS process ...

X-FAB unveils AMS Design Kit for Siemens' Tanner IC Design Tools

In addition, careful design and control of the organic-inorganic interface could afford hybridization of electronic states, enhancement of radiationless energy transfer or electron transfer, or ...

Membrane insertion of—and membrane potential sensing by—semiconductor voltage nanosensors: Feasibility demonstration

X-FAB released a reference design kit for Siemens EDA's Tanner analog/mixed-signal (AMS) software. It can show the set-up of the PDK for the company's XH018 180nm modular mixed-signal high-

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voltage ...

Week In Review: Design, Low Power

QDID IP produces 64 x 64 arrays of cells, each cell consisting of two transistors. The technology then exploits the quantum tunnelling that occurs through the CMOS oxide layer. Electrons propagate ...

Quantum-tunnelling semiconductor IP verified as secure

When it comes to integrated circuit (IC) design verification, the same principle holds true. A foundry's design rules, encapsulated in a foundry-qualified design rule checking (DRC) rule deck, ...

Signoff DRC In P&R Lets You Get Better Products To Market Faster

Transistors are often used as switches to drive digital logic circuits such as TTL, CMOS, or ECL. Triac Output Semiconductor device used primarily as a switch. A triac can conduct in both directions, ...

Programmable Logic Controllers (PLC) Specifications

Liz Allen, a former Apple marketing executive ... How would that change their attitudes towards you, and what new ideas might the design process uncover? Leading Australian CMOs and CIOs joined CMO ...

Leadership News, Features, and Interviews

Printed circuit boards (PCBs) are becoming increasingly complex as miniaturization is creating new challenges where reliability, productivity, and peak performance are critical. The roadmap for ...

Enabling next-gen advanced electronics, MKS Instruments will acquire Atotech

A leading-edge research firm focused on digital transformation. Good Subscriber Account active since Free subscriber-exclusive

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audiobook! “No Rules Rules: Netflix and the Culture of Reinvention ...

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